

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

Application Number	09765916
Filing Date	2001-01-18
First Named Inventor	Frederic Canut
Art Unit	2193
Examiner Name	Kang, Insun
Attorney Docket Number	00PA339US03

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	1	SU et al. "A Timing-Driven Soft-Macro Placement and Resynthesis Method in Interaction with Chip Floorplanning" IEEE Transaction on Computer-Aided Design of Integrated Circuits and Systems, April 1999, Vol 18, No. 4, pp. 475-483.	<input type="checkbox"/>
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